

**SILICON-ON-INSULATOR WAFER
AND METHOD OF MANUFACTURING THE SAME**

5

Abstract of the Disclosure

10 In a silicon-on-insulator (SOI) wafer and a method of manufacturing the same,
the SOI wafer includes a first semiconductor wafer including an isolation insulating film
formed to define an active region; a well region and a buried layer formed in the active
region of the first semiconductor wafer; and a second semiconductor wafer bonded with
the first semiconductor wafer, wherein an SOI insulating film, which contacts a lower
portion of the isolation insulating film and electrically insulates a lower portion of the
active region, is formed.

15

J:\sam\0422\422patapp3.doc